

Notice of Allowability

Application No.

09/711,113

Examiner

Ricardo Pizarro

Applicant(s)

GOODRICH II ET AL.

Art Unit

2661

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 7/8/04.
2. ☒ The allowed claim(s) is/are 1-4 and 8-10. They have been renumbered 1-7.
3. ☒ The drawings filed on 09 November 2000 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☐ All b) ☐ Some* c) ☐ None of the:
 1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☒ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☒ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

KENNETH VANDERPUYE
PRIMARY EXAMINER

Examiner's Amendment

1. An Examiner's amendment for the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee. Authorization for this examiner's amendment was given in a telephone interview with Mr. Albert Mettrailer on 9/7/04

2. **CLAIMS HAVE BEEN AMENDED AS FOLLOW:**

Claim 1

In line 19 "said inputs" has been inserted before -to a read cell- and before -and to-.

Claim 8 (entire claim)

A system for transferring a cell available status signal from a peripheral device to a CPU in telecommunications hub operating under UTOPIA protocol, comprising:

a buffer having an input coupled to a cell available output of a peripheral device, having an output connected to a UTOPIA cell available status bus, and having a control input for receiving a output enable signal;

address detection logic comprising a register for storing the address of the peripheral device, an input for receiving an address from the UTOPIA bus, and an output for providing an indication of when the UTOPIA bus address matches the stored address;

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a DFF having a data input coupled to the address detection logic output , a clock input coupled to the UTOPIA bus clock line, and an output coupled to the data buffer control input; and

wherein the cell available status signal is a read cell available status signal, the buffer output is coupled to the UTOPIA read cell available signal bus and , the address detection logic receives the read address signal from the 20 UTOPIA bus.

Claim 9 (entire claim)

A system for transferring a cell available status signal from a peripheral device to a CPU in telecommunications hub operating under UTOPIA protocol, comprising:

a buffer having an input coupled to a cell available output of a peripheral device, having an output connected to a UTOPIA Read data bus , and having a control input for receiving a output enable signal;

address detection logic comprising a register for storing the address of the peripheral device, an input for receiving an address from the UTOPIA bus, and an output for providing an indication of when the UTOPIA bus address matches the stored address;

a DFF having a data input coupled to the address detection logic output , a clock input coupled to the UTOPIA bus clock line, and an output coupled to the data buffer control input; and

wherein the cell available status signal is a transmit cell available status signal,

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the buffer output is coupled to the UTOPIA transmit cell available signal bus and, the address detection logic receives the transmit address signal from the UTOPIA bus.

Examiner's statement of reasons for Allowance

3. The following is an examiner's statement of reasons for allowance: No prior art of record was found for a system for transferring data from a peripheral device to a CPU in telecommunications hub operating under UTOPIA protocol comprising a data buffer having data inputs coupled to data outputs of a peripheral device, address detection logic comprising a register for storing the address of said device, a first DFF having a data input coupled to said detection device, a second DFF having a data input coupled to the UTOPIA bus receive enable line and wherein a first AND gate having inputs coupled to the outputs of the first DFF and second DFF, said inputs also coupled to a read cell available signal of the peripheral device and to the UTOPIA bus read enable line

4. Any comments considered necessary by applicant must be submitted no later than payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to **Ricardo Pizarro** whose telephone number is (7571) 272-3077. The examiner can normally be reached on Monday-Friday from 9:00 AM to 5:00 PM. The fax number for this Group is (703) 872-9306.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, **Kenneth Vanderpuye**, can be reached on (571) 272-3078.

9/8/2004

Ricardo M. Pizarro



KENNETH VANDERPUYE
PRIMARY EXAMINER